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<b>INFORMATION DISCLOSURE CITATION PTO-1449</b>	Atty. Docket No. 011118A	Serial No. 10/649,994
	Applicant(s): TAKAHASHI, Koji	
	Filing Date: August 28, 2003	Group Art Unit: 2826

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<u>Bu</u>	AA	5,408,115	Chang	04/18/95	257	324
<u>Bu</u>	AB	5,969,383	Chang et al.	10/19/99	257	316
<u>Bu</u>	AC	5,838,041	Sakagami et al.	11/17/98	257	324
<u>Bu</u>	AD	6,248,633	Ogura et al.	06/19/01	438	267
<u>Bu</u>	AE	5,467,308	Chang et al.	11/14/95	365	185.01

**FOREIGN PATENT DOCUMENTS**

Document No.	Date	Country	Translation (Yes or No)
<u>Bu</u> AF 09-252059	09/22/97	Japan	Abstract
<u>      </u> AG			
<u>      </u> AH			
<u>      </u> AI			
<u>      </u> AJ			

**OTHER DOCUMENTS**

<u>      </u>	AK	
<u>      </u>	AL	
Examiner	Date Considered	



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	Applicant(s): TAKAHASHI, Koji	
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### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
<del>_____</del>	<del>AA</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>
<del>_____</del>	<del>AB</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>
<del>_____</del>	<del>AC</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>
<del>_____</del>	<del>AD</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>
<del>_____</del>	<del>AE</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>	<del>_____</del>

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<u>BK</u>	AF 2000-0076792	12/26/00	Korea	No
<u>BK</u>	AG 2001-156188	6/08/01	Japan	Abstract only
<u>BK</u>	AH 05-326893	12/10/93	Japan	Abstract only
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### OTHER DOCUMENTS

<u>BK</u>	AK	Boaz Eitan et al., NROM: A Novel Localized Trapping, 2-Bit Nonvolatile Memory Cell, IEEE ELECTRON DEVICE LETTERS, November, 2000, VOL. 21, NO. 11, PP. 543-545.
Examiner <u>Brook Kebede</u> Date Considered <u>12/21/2004</u>		